





BYE

I1 [24] Nivaldo NUNES

[24] Nivaldo NUNES

Default

I2 [16] Ruben TEIXEIRA

[16] Ruben TEIXEIRA

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I3 [14] Filipe VARELA

[14] Filipe VARELA

7-11, 12-10, 11-13, 5-11 (27m)

I4 [18] João FAVILA VIEIRA

[18] João FAVILA VIEIRA

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I5 [21] Paulo CâMARA

[21] Paulo CâMARA

11-6, 11-9, 11-5

I6 [20] Marco FREITAS

[20] Marco FREITAS

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I7 [17] Marco Filipe Gomes SARDINHA

[17] Marco Filipe Gomes SARDINHA

7-11, 11-9, 12-14, 11-7

I8 [22] Miguel CAMACHO

[22] Miguel CAMACHO

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7-11, 11-9, 11-9, 11-6

[16] Ruben TEIXEIRA

5-11, 5-11, 4-11

[17] Marco Filipe Gomes SARDINHA

[18] João FAVILA VIEIRA	
3-11, 8-11, 6-11 (12m)	[21] Paulo CâMARA
[21] Paulo CâMARA	

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K1	[14] Filipe VARELA
[14] Filipe VARELA	K3 ADG Sat Nov 26, 3pm, Ct. 1
[20] Marco FREITAS	
Default	[20] Marco FREITAS
[22] Miguel CAMACHO	

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The diagram illustrates a multi-stage merge sort algorithm. It shows the recursive splitting of an array of 7 elements (M1 to M7) into smaller sub-arrays until single elements are reached, followed by the merging of these sub-arrays back into sorted order.

Splitting Phase (Left to Right):

- Stage 1:** The initial array [M1, M2, M3, M4, M5, M6, M7] is split into [M1, M2, M3, M4] and [M5, M6, M7].
- Stage 2:** [M1, M2, M3, M4] is split into [M1, M2] and [M3, M4]. Similarly, [M5, M6, M7] is split into [M5, M6] and [M7].
- Stage 3:** [M1, M2] is split into [M1] and [M2]. [M3, M4] is split into [M3] and [M4]. [M5, M6] is split into [M5] and [M6]. [M7] remains as a single element.

Merging Phase (Right to Left):

- Stage 4:** [M1] and [M2] are merged into [M1, M2]. [M3] and [M4] are merged into [M3, M4]. [M5] and [M6] are merged into [M5, M6]. [M7] remains as a single element.
- Stage 5:** [M1, M2] and [M3, M4] are merged into [M1, M2, M3, M4]. [M5, M6] and [M7] are merged into [M5, M6, M7].
- Stage 6:** [M1, M2, M3, M4] and [M5, M6, M7] are merged into the final sorted array [M1, M2, M3, M4, M5, M6, M7].

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