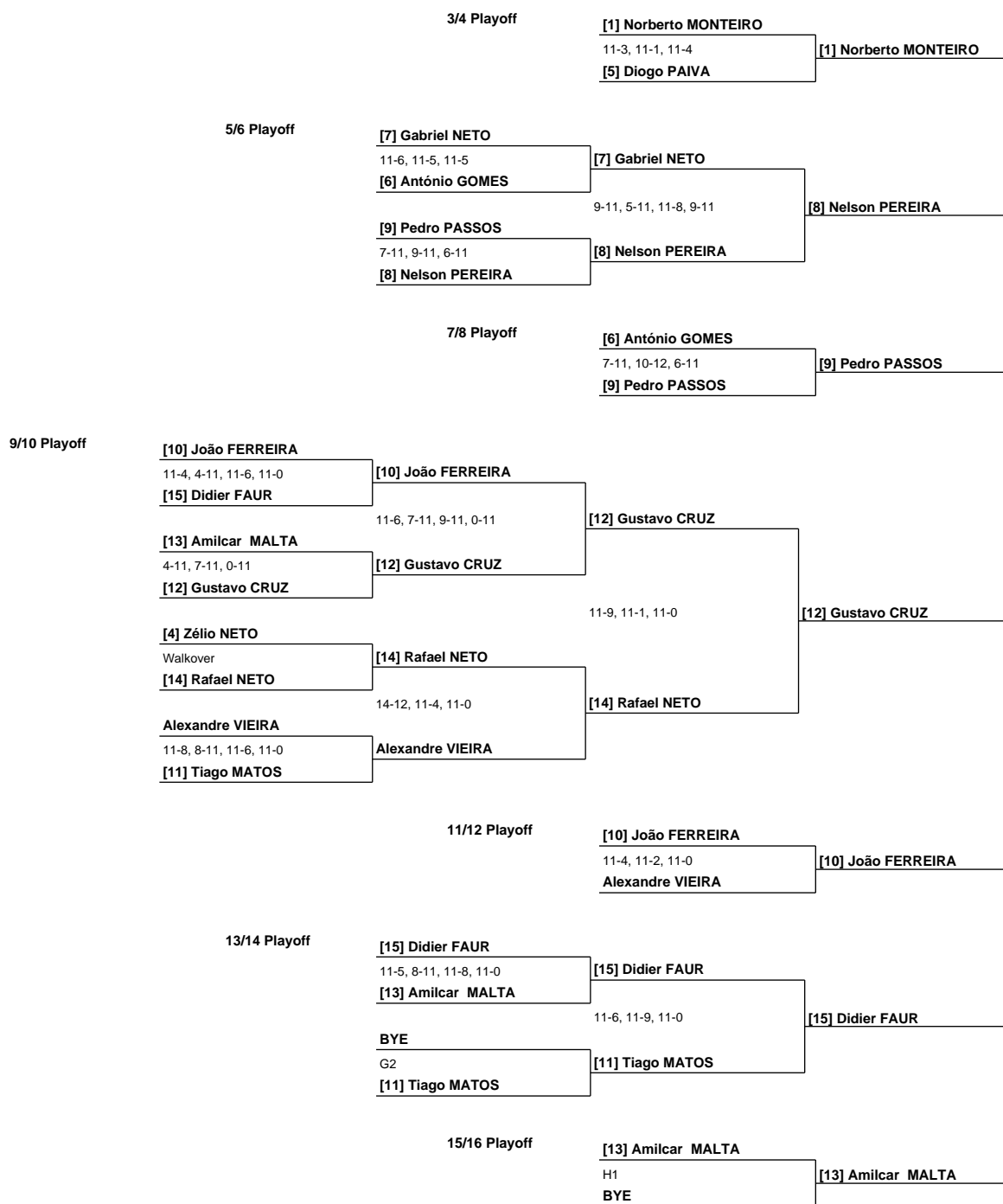


[illegible]



BYE					
I1	BYE				
BYE					
	I9	Pedro GOUVEIA			
Pedro GOUVEIA					
I2	Pedro GOUVEIA				
BYE					
		I13	Pedro GOUVEIA		
BYE					
I3	BYE				
BYE					
	I10	BYE			
BYE					
I4	BYE				
BYE					
			4-11, 5-11, 0-11	João Ferreira COIMBRA	
BYE					
I5	BYE				
BYE					
	I11	João Ferreira COIMBRA			
João Ferreira COIMBRA					
I6	João Ferreira COIMBRA				
BYE					
		Walkover	João Ferreira COIMBRA		
BYE					
I7	[16] José MONTEIRO				
[16] José MONTEIRO					
	I12	[16] José MONTEIRO			
BYE					
I8	BYE				
BYE					

BYE	
J1	
BYE	

The diagram illustrates a sequence of signals on a 4-bit bus. The master (top) sends a 'BYE' signal, followed by a 'K1' signal, then another 'BYE' signal. The slave (bottom) responds with a 'BYE' signal, followed by a 'K3' signal, then another 'BYE' signal. The 'K3' signal is a pulse that occurs after the master's second 'BYE' signal and before the slave's second 'BYE' signal.

BYE	
L1	
BYE	

The diagram illustrates a multi-stage merge sort algorithm. It starts with four input arrays: M1, M2, M3, and M4. Each array is followed by a box labeled "BYE".

- M1 and M2 are merged into M5.
- M3 and M4 are merged into M6.
- M5 and M6 are merged into M7.

The final output is M7.

27/28 Playoff

BYE

N1

BYE

29/30 Playoff

BYE

O1

BYE

BYE

O3

BYE

O2

BYE

BYE

31/32 Playoff

BYE

P1

BYE