

Main Draw

A1	[1] Norberto MONTEIRO
BYE	

BYE	
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11-6, 11-1, 11-5 [1] Norberto MONTEIRO

1

BYE

A4 [7] Gabriel NETO

[3] Rui CRUZ

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1

[3] Rui CRUZ

1

BYE

A8 [3] Rui CRUZ

[3] Rui CRUZ

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[5] Diogo PAIVA

1

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[2] André LIMA

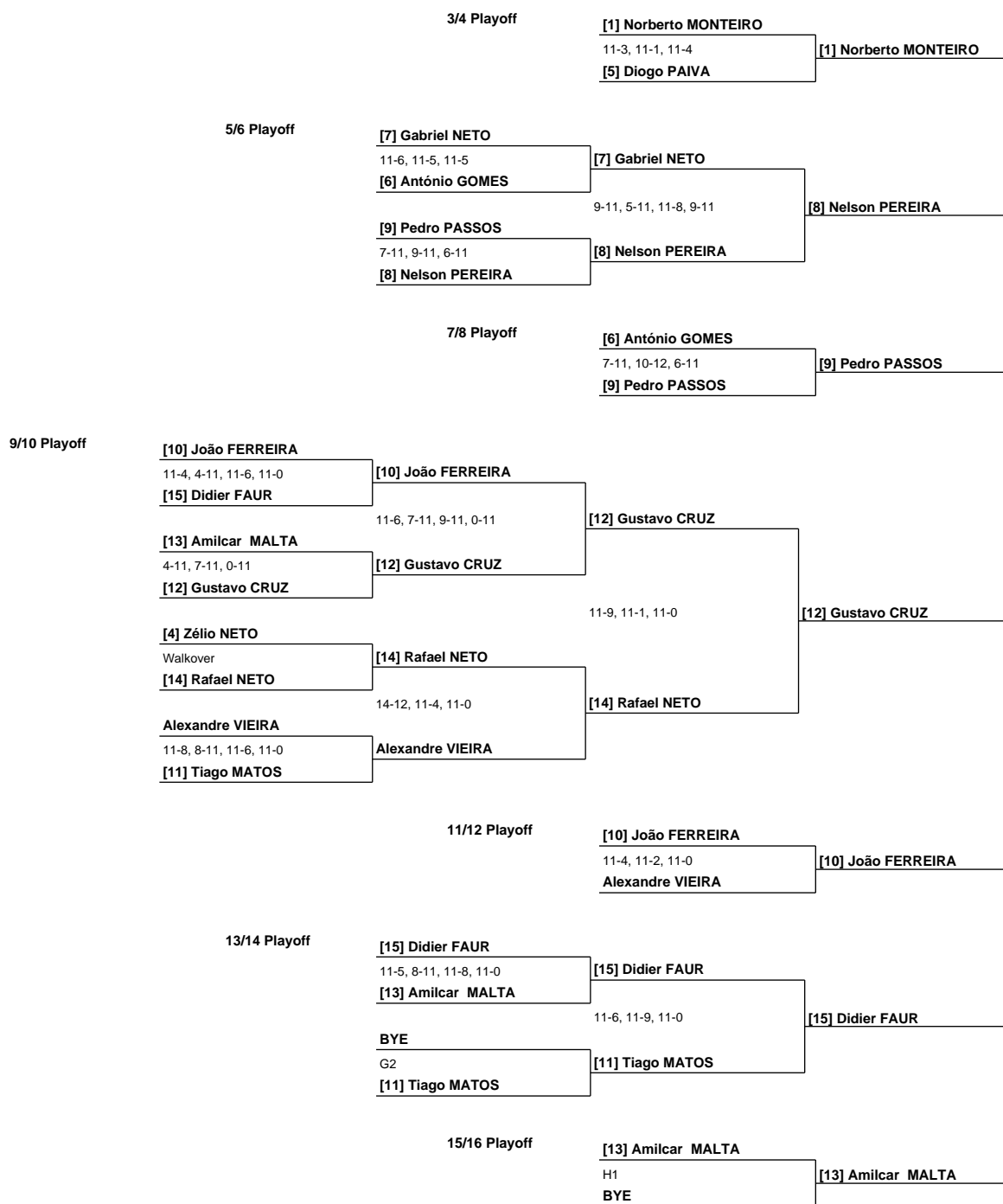
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[2] André LIMA

1

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BYE					
I1	BYE				
BYE					
	I9	Pedro GOUVEIA			
Pedro GOUVEIA					
I2	Pedro GOUVEIA				
BYE					
		I13	Pedro GOUVEIA		
BYE					
I3	BYE				
BYE					
	I10	BYE			
BYE					
I4	BYE				
BYE					
			4-11, 5-11, 0-11	João Ferreira COIMBRA	
BYE					
I5	BYE				
BYE					
	I11	João Ferreira COIMBRA			
João Ferreira COIMBRA					
I6	João Ferreira COIMBRA				
BYE					
		Walkover	João Ferreira COIMBRA		
BYE					
I7	[16] José MONTEIRO				
[16] José MONTEIRO					
	I12	[16] José MONTEIRO			
BYE					
I8	BYE				
BYE					

BYE	
J1	
BYE	

The diagram illustrates a 4-bit bus system with a master and a slave. The master's signals are: **BYE**, **K1**, **BYE**. The slave's signals are: **BYE**, **K3**, **BYE**. The **K3** signal is a pulse that occurs after the master's second **BYE** signal and before the slave's second **BYE** signal.

BYE	
L1	
BYE	

The diagram illustrates a multi-stage merge sort algorithm. It starts with four input arrays: M1, M2, M3, and M4. Each array is represented by a box with 'BYE' written above it. M1 and M2 are merged into M5, M3 and M4 are merged into M6, and then M5 and M6 are merged into M7. The final output is M7, which is represented by a large box with 'BYE' written above it. The merging process is shown by lines connecting the boxes, with 'BYE' labels indicating the merge operation.

27/28 Playoff

BYE

N1

BYE

29/30 Playoff

BYE

O1

BYE

BYE

O3

BYE

O2

BYE

BYE

31/32 Playoff

BYE

P1

BYE